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(combinatorial, sequential, state machines, and FPGAs), thus providing a self-contained single reference for the teaching of digital circuit design with VHDL.

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Prof. Volnei A. Pedroni. The author received his MSc and PhD degrees in Electrical Engineering from the California Institute of Technology (Caltech), with emphasis on VLSI design, analog and digital. He is a Professor of Electronics Engineering at Brazil's UTFPR and a regular Visiting Faculty of Electrical Engineering at

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Caltech. He has done collaboration also with other Institutions, in USA and Italy (he is a Brazilian and Italian citizen).

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